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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete If Known</b>	
				Applicati n / Conf. N .	Unknown /
				Filing Dat	August 25, 2003
				First Nam d Inventor	Warren E. Cory
				Art Unit	Unknown
				Examiner Name	Unknown
				Attorney Docket Number	X-1065-2P US
Sheet	1	of	2		

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Examiner Signature	Now <i>TE</i>	Date Considered	3/9/05
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

\* Applicant's unique citation designation number (optional). \* See Kinds of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. \* Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). \* For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. \* Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. \* Applicant is to place a check mark here if English language Translation is attached.

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Sheet 2 of 2

**Complete if Known**

Applicant / Conf. No.	Unknown /
Filing Date	August 25, 2003
First Named Inventor	Warren E. Cory
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	X-1065-2P US

**OTHER -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
he		Cary D. Snyder and Max Baron; "Xilinx's A-to-Z System Platform"; Cahners Microprocessor; The Insider's Guide to Microprocessor Hardware; Microdesign Resources; February 6, 2001; pp. 1-5.	
he		Xilinx, Inc.; "Virtex-II Platform FPGA Handbook"; published December 2000, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp. 33-75.	

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DON CE

Date  
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3/9/05

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